

**M4 SERIES P6LG-XXXXE 3KV ISOLATED 0,6 – 1,5 W REGULATED SINGLE OUTPUT SIP7**

**Available Inputs: 5, 12 and 24 VDC**

**Available Outputs: 3.0, 3.3, 5, 9, 12 and 15 VDC**

Other specifications please enquire.

**Electrical Specifications**

(Typical at + 25° C, nominal input voltage, rated output current unless otherwise specified)

**Input Specifications**

Voltage range +/- 10 %  
 Filter Capacitors

**Isolation Specifications**

Rated voltage 3000 VDC  
 Leakage current 1 mA  
 Resistance 10<sup>9</sup> Ohm  
 Capacitance 60 pF typ.

**Output Specifications**

Voltage accuracy +/- 1 %, max.  
 Ripple and noise (at 20 MHz BW) 50 mV p-p, max.  
 Short circuit protection Short term  
 Line voltage regulation +/- 0,5 % max.  
 Load voltage regulation +/- 0,5 % max.  
 Temperature coefficient +/- 0,02 % / °C

**General Specifications**

Efficiency 60 % to 75 %  
 Switching frequency 125 KHz, typ.

**Environmental Specifications**

Operating temperature (ambient) - 40° C to + 85° C  
 Storage temperature - 55 °C to + 125 °C  
 Derating See graph  
 Humidity Up to 90 %, non condensing  
 Cooling Free air convection

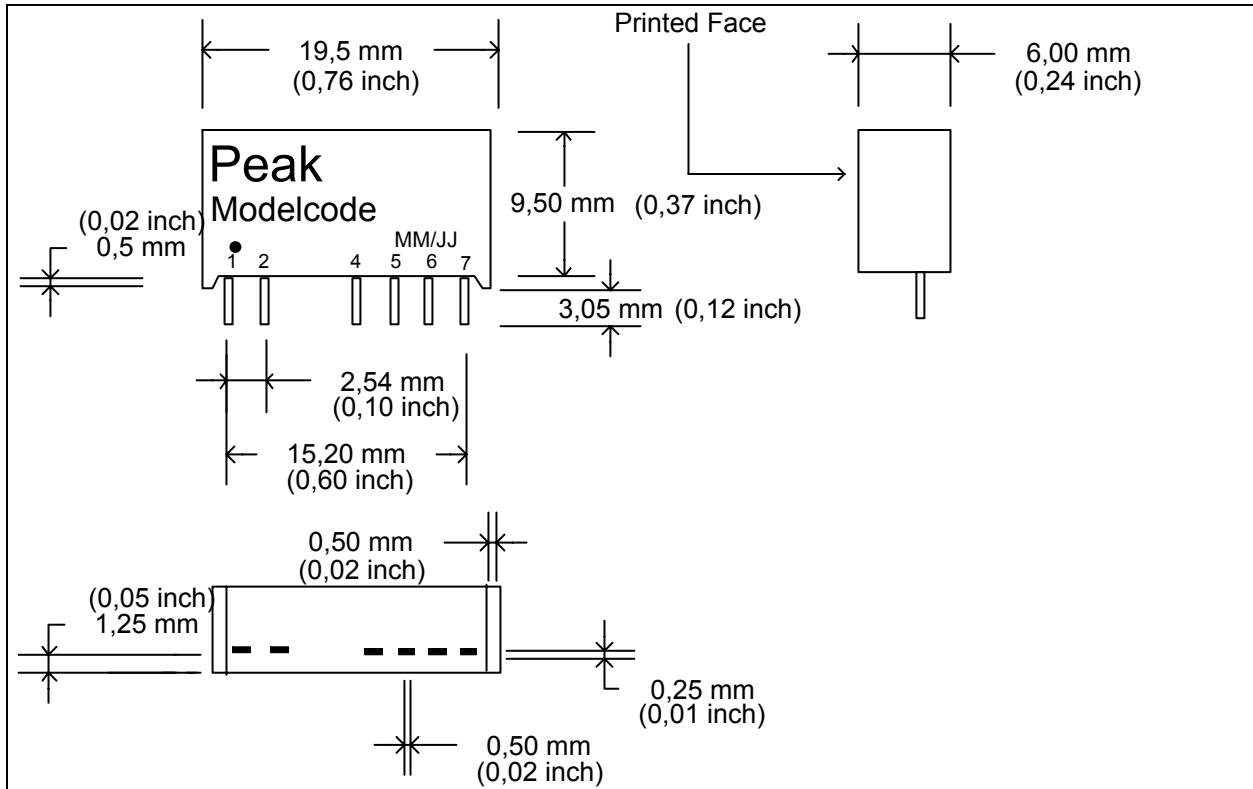
**Physical Characteristics**

Dimensions SIP 19,50 x 6,00 x 9,50 mm  
 0,76 x 0,24 x 0,37 inches  
 Weight 2,0 g  
 Case material Non conductive black plastic

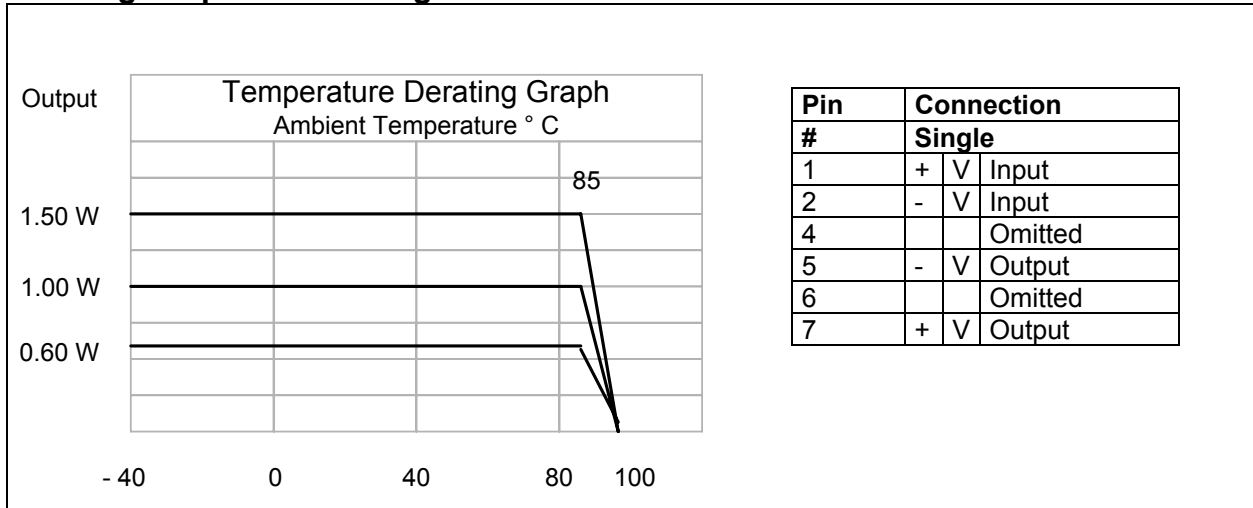
**Examples of Partnumbers/Modelcode**

PART NO.	INPUT VOLTAGE (VDC)	INPUT CURRENT NO LOAD	INPUT CURRENT FULL LOAD	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (max. mA)	EFFICIENCY FULL LOAD (% TYP.)
P6LG-0503E	5	18	193	3	200	62
P6LG-053R3E	5	18	212	3,3	200	62
P6LG-054R8E	5	22	307	4,85	200	63
P6LG-0505E	5	26	312	5	200	64
P6LG-0509E	5	26	421	9	150	64
P6LG-0512E	5	26	375	12	100	64
P6LG-1203E	12	15	81	3	200	62
P6LG-123R3E	12	15	88	3,3	200	62
P6LG-124R8E	12	20	128	4,85	200	63
P6LG-1205E	12	23	130	5	200	64
P6LG-1209E	12	23	176	9	150	64
P6LG-1212E	12	23	156	12	100	64
P6LG-2403E	24	12	40	3	200	62
P6LG-243R3E	24	12	43	3,3	200	63
P6LG-244R8E	24	10	64	4,85	200	63
P6LG-2405E	24	10	64	5	200	65
P6LG-2409E	24	9	88	9	150	64
P6LG-2412E	24	9	78	12	100	64

**Dimensions**



**Derating Graph and Pinning**



Pin #	Connection
1	+ V Input
2	- V Input
4	Omitted
5	- V Output
6	Omitted
7	+ V Output